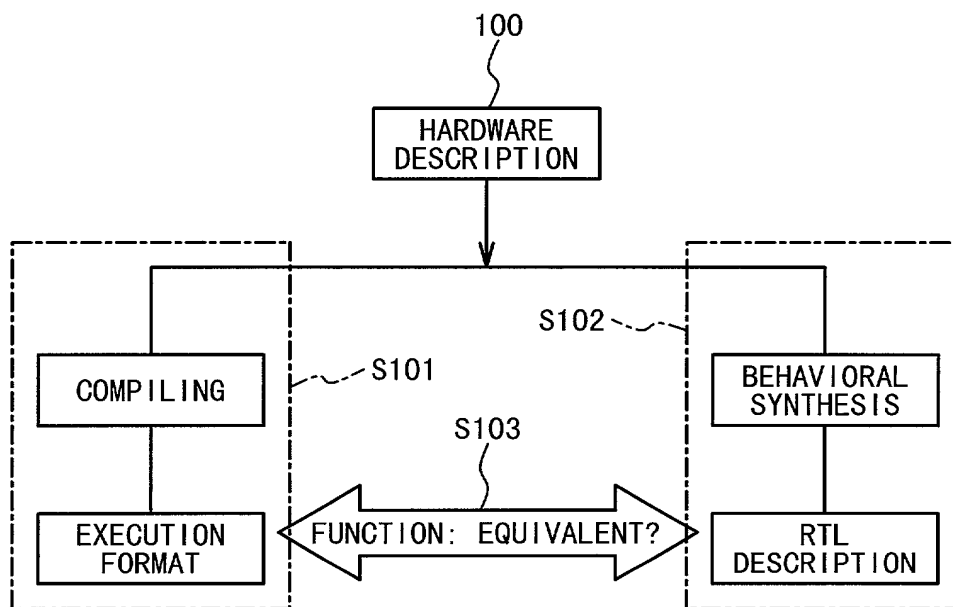
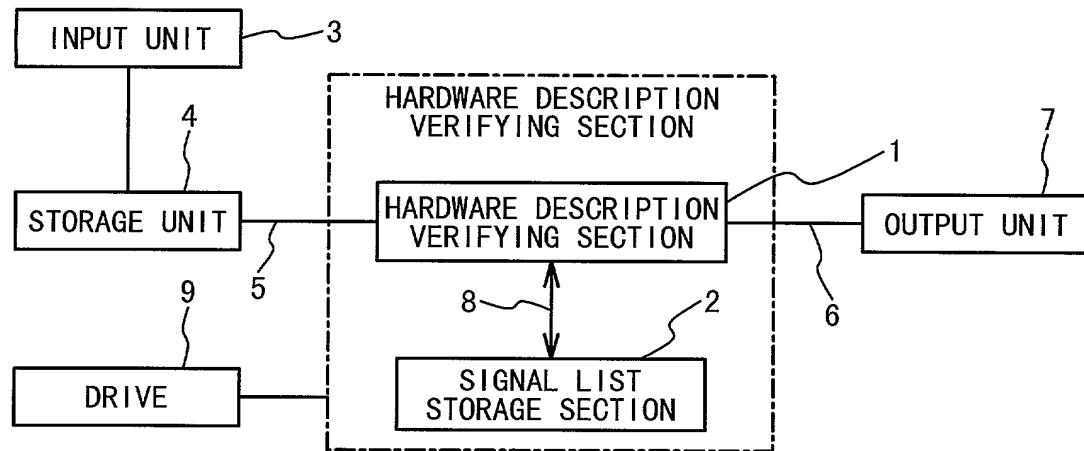


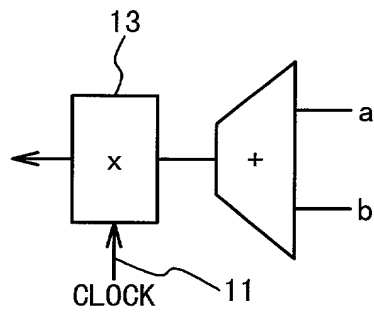
Fig. 1 PRIOR ART



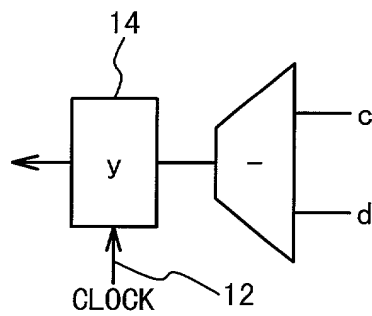
# Fig. 2



# Fig. 3



# Fig. 4



F i g . 5

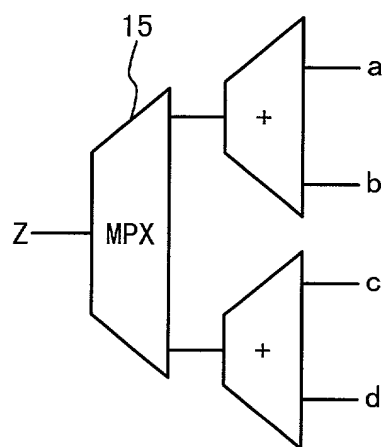


Fig. 6

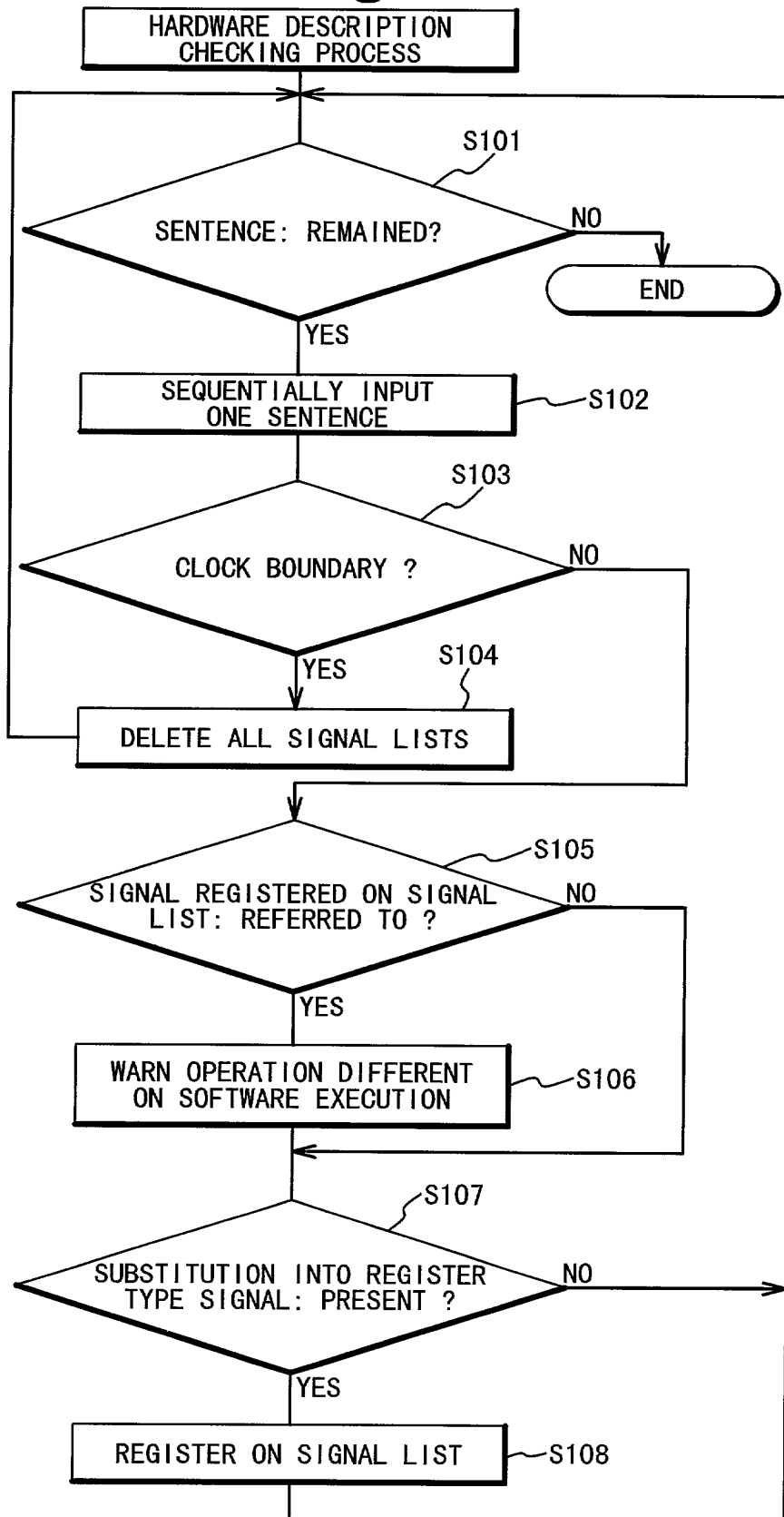


Fig. 7

```

/* REG.C */

#ifdef C
# define REG UNSIGNED INT
#endif

MAIN()

/* C */
/* HDL */
/* LIST */

    INT T;
    REG x, y;

    x = 0;
    CLOCK();
    x = 1;
    t = 3;
    y = x + t;
    CLOCK();

/* x=0 */
/* x=1 */
/* t=3 */
/* y=4 */

/* {x} */
/* {} */
/* {x} */
/* {x} */
/* {x, y} */
/* {} */

/* S107 */
/* S103 */
/* S107 */

/* S105, S107 */
/* S103 */

```

Fig. 8

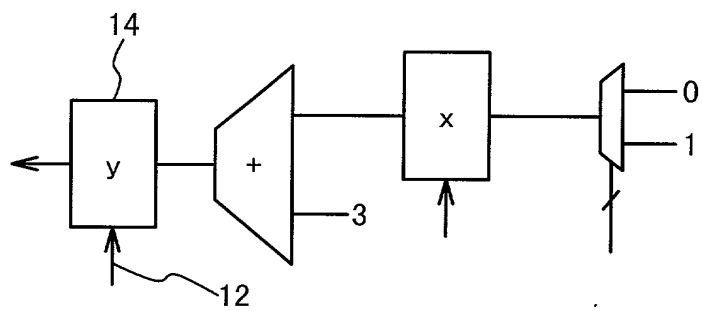


Fig. 9

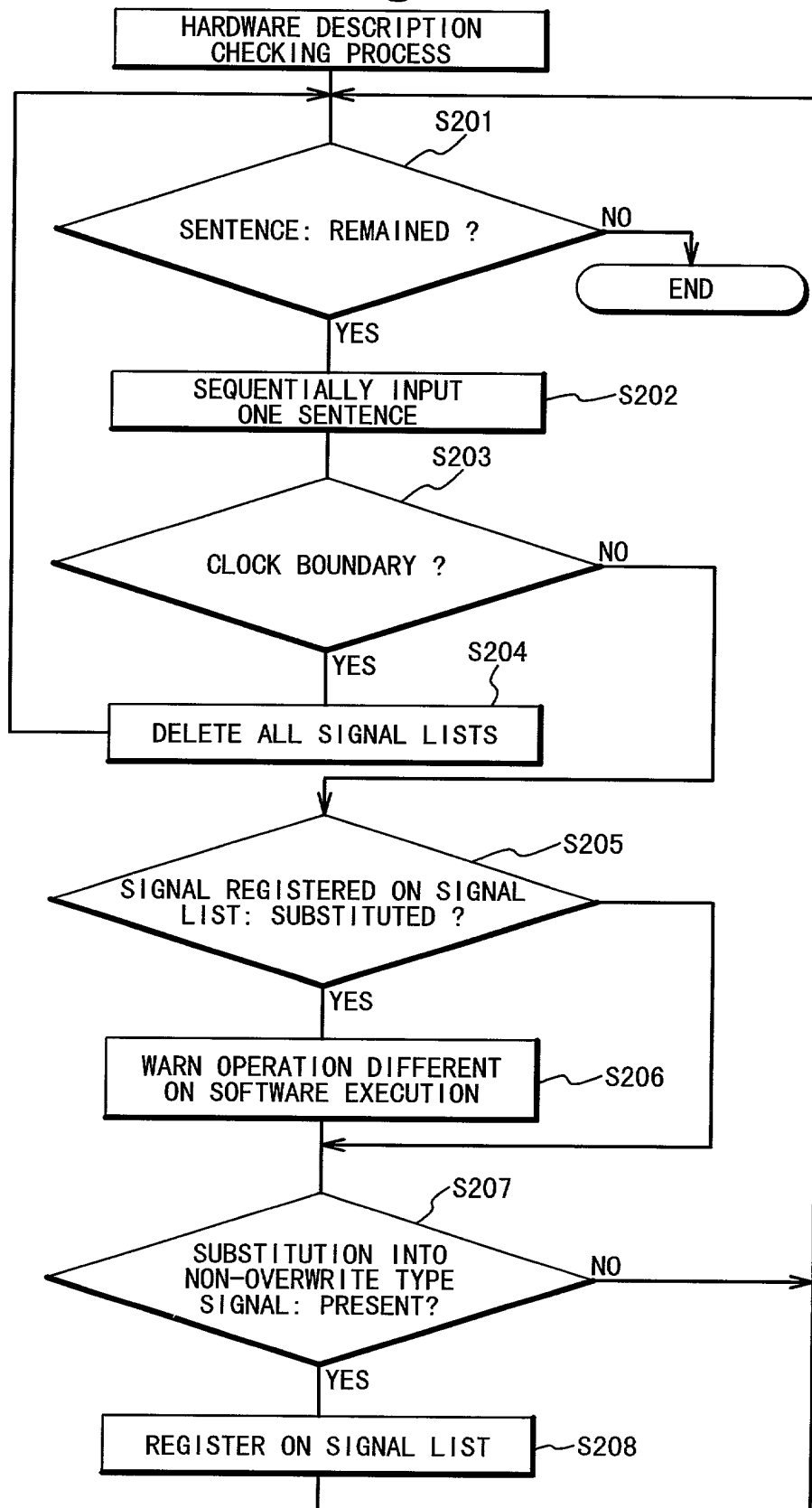


Fig. 10

```

/* ASSIGN. C */

#ifdef C
#define TER UNSIGNED INT
#endif

MAIN()
{
    TER z, t;

    z = 0;
    CLOCK();
    z = 1;
    t = 3;
    z = t + 2;
    CLOCK();

    /* C */
    /* HDL */
    /* LIST */

    /* z=0 */
    /* z=1 */
    /* t=3 */
    /* z=5 */

    /* z=0 */
    /* z=1 */
    /* t=3 */
    /* z=5 */

    /* {z} */
    /* {} */
    /* {z} */
    /* {z, t} */
    /* {z, t} */
    /* {} */

    /* S207 */
    /* S203 */
    /* S207 */
    /* S207 */
    /* S205, S207 */
    /* S203 */
}

```



F i g . 1 1

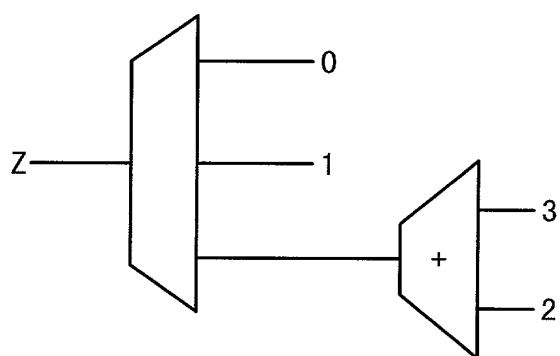


Fig. 12

```

/* TER.C */

#ifdef C
# define TER UNSINED INT
#endif

MAIN()
{
    TER z, t;

    t = 3;
    CLOCK();
    z = t + 2;
    CLOCK();

    /* C */
    /* HDL */
    /* LIST */

    /* {t} */
    /* {} */
    /* {z} */
    /* {} */

```

F i g . 1 3

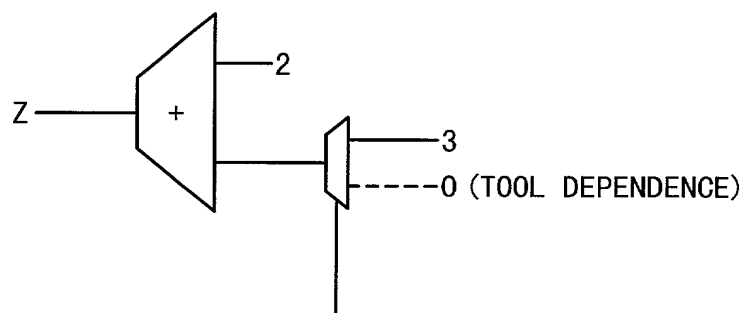


Fig. 14

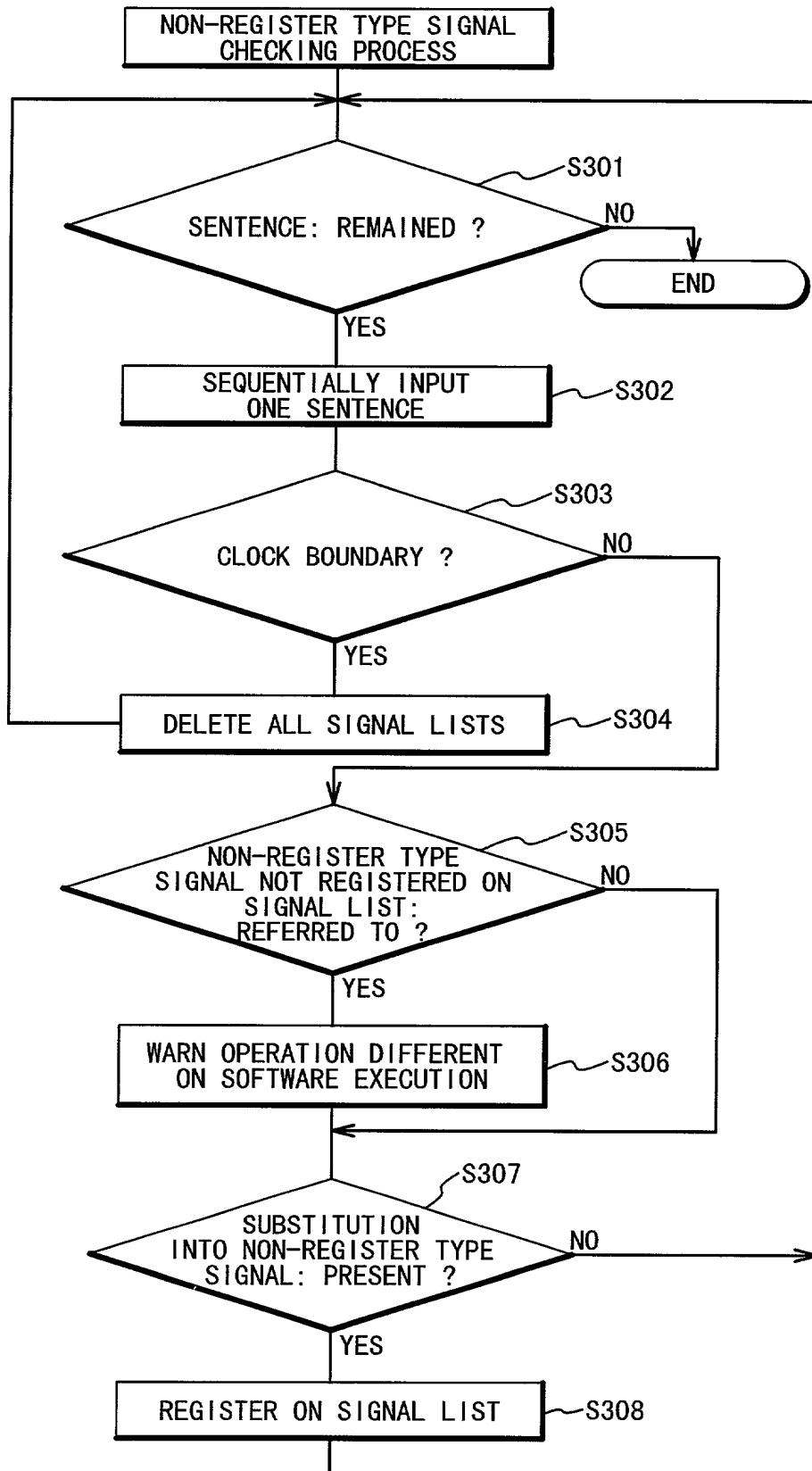


Fig. 15

```

/* WIRE, C */

#ifdef C
# define TER UNSIGNED INT
#endif

MAIN()
{
    TER z, t;

    t = 3;
    CLOCK();
    z = t + 2;
    t = 1;
    CLOCK();

    /* C */
    /* HDL */
    /* LIST */

    /* {t} */
    /* {} */
    /* {z} */
    /* {z, t} */
    /* {} */

```

F i g . 1 6

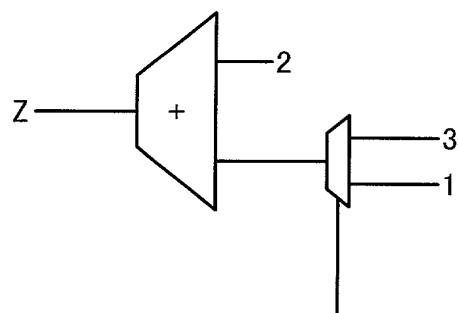


Fig. 17

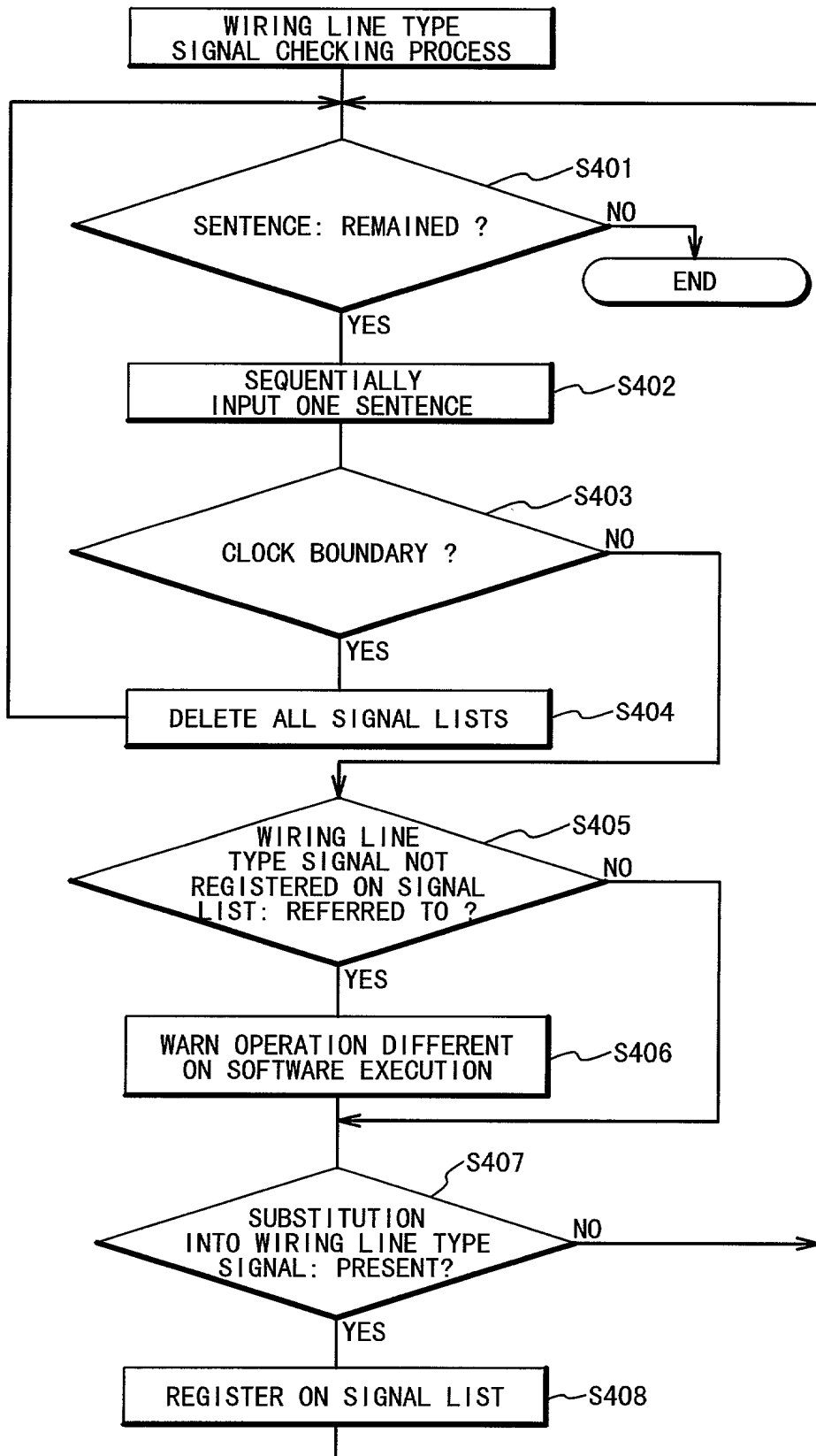


Fig. 18

```

/* AND.C */

MAIN()
{
    INT a, i;

                                /* C */
                                /* HDL */

    i = 0;
    a = 0;
    CLCOK();
    if(i >0 && a++) {
        i = 0;
    }
    CLCOK();

                                /* i=0 */
                                /* a=0 */
                                /* a=1 */
}

```



Fig. 19

